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Substitute for form 1449A/PTO		Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		<i>Application Number</i>	10/647,768
		<i>Filing Date</i>	August 25, 2003
		<i>First Named Inventor</i>	S. Brandon Keller
		<i>Art Unit</i>	2825
		<i>Examiner Name</i>	S. WHITMORE
Sheet	1	<i>Attorney Docket Number</i>	100111232-1
(Use as many sheets as necessary)			

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

10/647 768

Filing Date August 25, 2003

First Name/Initials: S. Brandon Kellar

1. SERVICE INVESTED. 3. Brandon
2011/03 2935

Examiner Name S. J. H. Tammek

Atmospheric Rocket

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

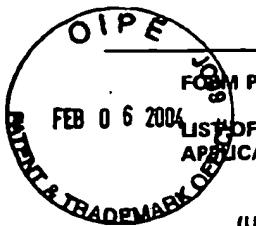
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PATENT APPLICATION

Sheet 1 of 6



FORM PTO-1449

FEB 06 2004

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
100111232-1	10/647,768	
APPLICANT	S. Brandon Keller, et al.	
FILING DATE	GROUP	
Aug. 25, 2003	2825	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
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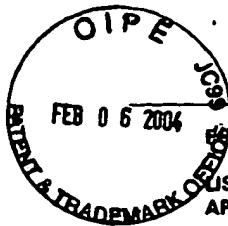
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>SN</i>	1Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
<i>SN</i>	1R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
<i>SN</i>	1S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,378, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003

EXAMINER

DATE CONSIDERED

4/13/05



PATENT APPLICATION

Sheet 2 of 6

FEB 06 2004

FORM PTO-1449

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APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

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GROUP

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2Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
2R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003
2S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,692, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

EXAMINER

DATE CONSIDERED

4/13/05


PATENT APPLICATION
Sheet 3 of 6

FEB 06 2003 FORM PTO-1449	ATTY. DOCKET NO. 100111232-1	APPLICATION NO. 10/647,768	CONFIRMATION NO.
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3Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655516US; Entitled: Method And Program Product For Determining Worst Case Currents In A Digital Integrated Circuit Through Worst-Case Superposition Of Partial Currents; Docket No.: 200300123-1; Filed January 30, 2004
3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655564US; Docket: 200308901-1; Entitled: Systems And Methods For Re-Using Circuit Design Analysis Results; Filed January 30, 2004

EXAMINER
DATE CONSIDERED

4/13/05



PATENT APPLICATION

Sheet 4 of 6

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
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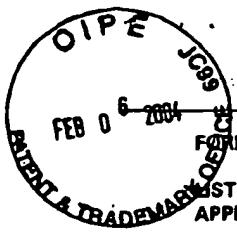
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4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655581US; Docket No.: 200308899-1; Entitled: System And Method For Determining Detail Of Analysis In A Circuit Design; Filed January 30, 2004
4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655595US; Docket No.: 200308898-1; Entitled: Systems And Methods That Identify Equivalent Instantiation-Specific Configuration Information For Analysis Tools; Filed January 30, 2004
4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H. & STEVENS, Scott Alan; U.S. Patent Application filed under EV210655520US; Entitled: System And Method To Limit Analyzed Current Flow In A Circuit Design; Filed January 30, 2004

EXAMINER

DATE CONSIDERED

4/13/05


PATENT APPLICATION
Sheet 5 of 6
FORM PTO-1449
**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT**
(Use several sheets if necessary)
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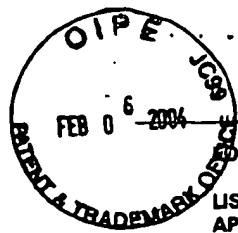
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<i>WW</i>	5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655533US; Docket: 200310172-1; Entitled: System And Method For Processing Configuration Information; Filed January 30, 2004
<i>WW</i>	5R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655555US; Docket: 200310170-1; Entitled: System And Method For Balancing Run-Time And Result Accuracy In A Circuit Design Analysis Tool; Filed January 30, 2004
<i>WW</i>	5S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655578US; Docket: 200308900-1; Entitled: System And Method For Indicating Logic State Combinations Used During Circuit Design Analysis; Filed January 30, 2004

EXAMINER
DATE CONSIDERED
4/13/05



PATENT APPLICATION

Sheet 6 of 6

FORM PTO-1449

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APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	6Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655547US; Docket: 200310171-1; Entitled: System And Method For Determining Control Signal Combinations For Use During Simulation Of A Stage Of A Circuit Design; Filed January 30, 2004
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	6S	
EXAMINER		DATE CONSIDERED
		4/13/05